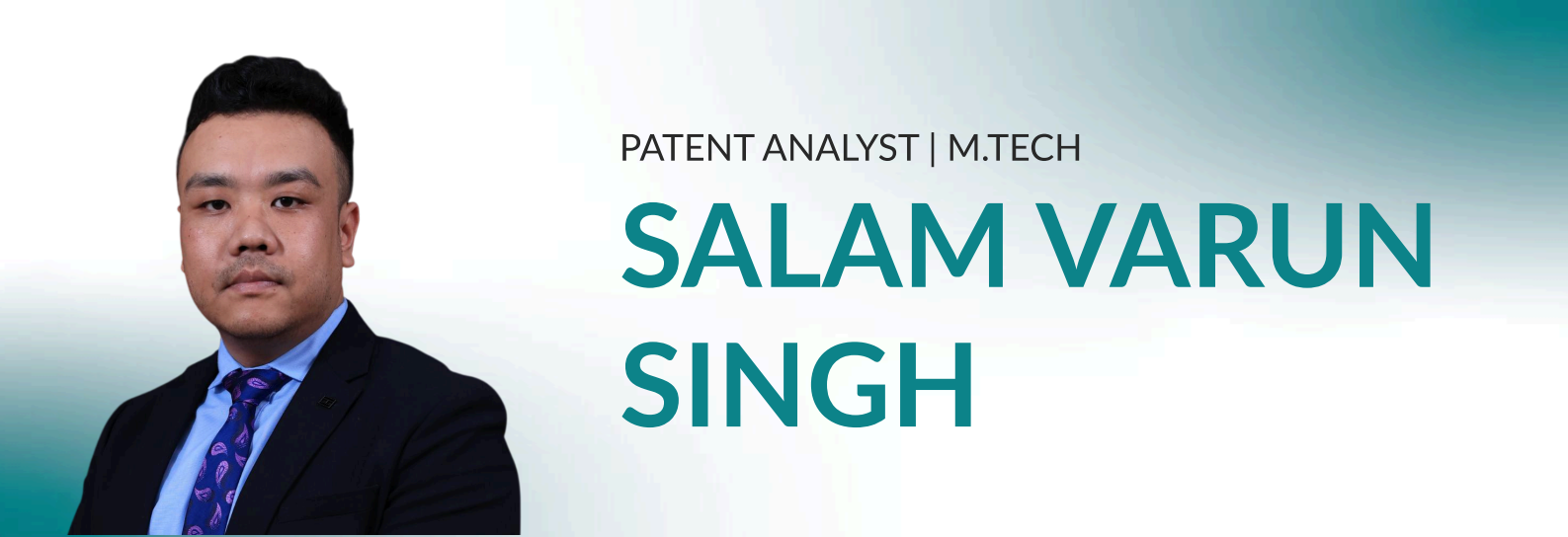




PATENT ANALYST | M.TECH

SALAM VARUN SINGH

Varun is a patent analyst at Kommit Techno-Legal LLP which holds a Master's degree with specialisation in Microelectronics and VLSI circuit design that comes under Electronics and Communication Engineering and also specialises in domains such as semiconductors design and fabrication, SoC architecture, automotive electronics, information security, databases, digital and analog IC design, financial systems, IoT hardware, artificial intelligence (AI) and machine learning (ML), augmented reality (AR) and virtual reality (VR), blockchain technology, wireless and mobile communication. He is skilled in patent analysis, including infringement studies, technology mapping (EOU), and claim interpretation. He is adept at maintaining safety compliance and collaborating within cross-functional teams to achieve organizational scalability and profitability.

PROFESSIONAL EXPERIENCE

Patent Analyst | **Kommit Techno-Legal LLP** . **2026 - Present**

- Conducts prior art searches and analyses for patent projects.
- Assists in the legal research for prosecution and litigation matters.
- Assists in patent consulting projects.

Patent Engineer L-2 | **Intellect Partners** **2025 - 2026**

- Area of work includes Electronics, Computer Science and Networking.
- Skills utilised: Patent Analysis, Patentability Searches, Intellectual Property Infringement, 3GPP Standards, Video Encoding Standards, 5G and Wireless Networking.
- Expanding expertise in Information security, Databases, Cloud computing, Control systems, and more.

PROFESSIONAL EXPERIENCE

Research Associate | [AnovIP](#)

2024 - 2025

- Developed skills on Infringement Searching, Intellectual Property Searching, EoU Charting Searching, Patent ranking and Feasibility Studies.
- Worked on 100+ Evidence of Use (EoU) charts illustrating licensing opportunities with top US companies in the domain of Semiconductor design and fabrication, SoC architecture, Automotive electronics, Payment processing, IoT hardware, Augmented reality and Virtual reality, Wireless and Mobile Communication.

EDUCATION

Master of Technology (M.Tech) | [MIT \(Manipur Institute of Technology\), Manipur VLSI and Microelectronics Engineering](#)

Bachelor of Technology (B.Tech) | [MIT \(Manipur Institute of Technology\), Manipur Electronics and Communication Engineering](#)

PROJECTS

Low power Full Adder using MICROWIND EDA Tool

2023

Microcontroller Based Digital Door Locking System

2015

SKILLS

Software

C++, Matlab, Microwind

Intellectual Property

InPASS, Espacenet, USPTO public search, WIPO PatentScope, IP searching, EoU Charting, Patent Ranking, Feasibility Studies.