



SENIOR PATENT ANALYST | B.E.

SATENDER KUMAR

Satender is an electronics and communications engineer, working as a Patent Analyst at Kommit. Satender is part of the patent consulting team and specialises in large portfolio analyses and infringement searches. He also works on patent prosecution projects, preparing patent specifications and communications to the patent office.

PROFESSIONAL EXPERIENCE

Patent Analyst | **Kommit Techno-Legal LLP**

2023 - Present

- Patent portfolio review and analyses
- Patent drafting
- Performs prior art searches and prior art analysis.
- Performs infringement searches and creates claim mapping tables and claim charts for patent enforcement projects.

EDUCATION

Bachelor of Engineering (B.E.) | **Netaji Subhas University of Technology**

Electronics and Communication Engineering

ACADEMIC PROJECTS

- Developed a brain game titled "Count The Matching Color" using Various 8085 Microprocessor Concepts and Techniques. PCB schematics were designed using Autodesk Eagle.
- Linear Enhanced V-to-I Converter Based on MOS Resistive Source Degeneration - applied various electronics principles with a focus to enhance the efficiency and also reduce power consumption of MOS technology.

SKILLS

Intellectual Property

InPass, Espacenet, USPTO public search, WIPO PatentScope

Technical

C, VHDL, Arduino, PSpice, AutoDesk Eagle, MATLAB, 8085 Microprocessor

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