



SENIOR PATENT ANALYST | B.TECH, M.TECH

VARUN REDDY

Varun is an electrical and electronics engineer, specialising in nanotechnology. He is a Senior Patent Analyst at Kommit and primarily focuses on delivery of consulting and research services to clients.

PROFESSIONAL EXPERIENCE

Senior Patent Analyst | **Kommit Techno-Legal LLP** **2023 - Present**

- Conducts infringement searches and develops claim charts and mapping tables for patent enforcement campaigns.
- Specialises in patent portfolio analyses.
- Conducts Freedom-to-Operate (FTO) searches and prior art analyses on patent filing and enforcement projects.

Patent Associate | **Hourglass Research** **2023**

EDUCATION

M.Tech in Nanotechnology | **Maulana Azad National Institute Of Technology**

M.Tech in VLSI System | **National Institute Of Technology Tiruchirapalli**

B.Tech in Electrical and Electronics Engineering | **Jayamukhi Institute Of Technological sciences**

PUBLICATIONS

Enhancing Efficiency and Antireflection Behaviour of CdTe Thin-Film Solar Cells with Magnesium Zinc Oxide (MZO) High-Resistance Transparent Layer (Under Review - IJLEO-D-23-03187)

SKILLS

Intellectual Property

InPass, Espacenet, USPTO public search, WIPO PatentScope

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